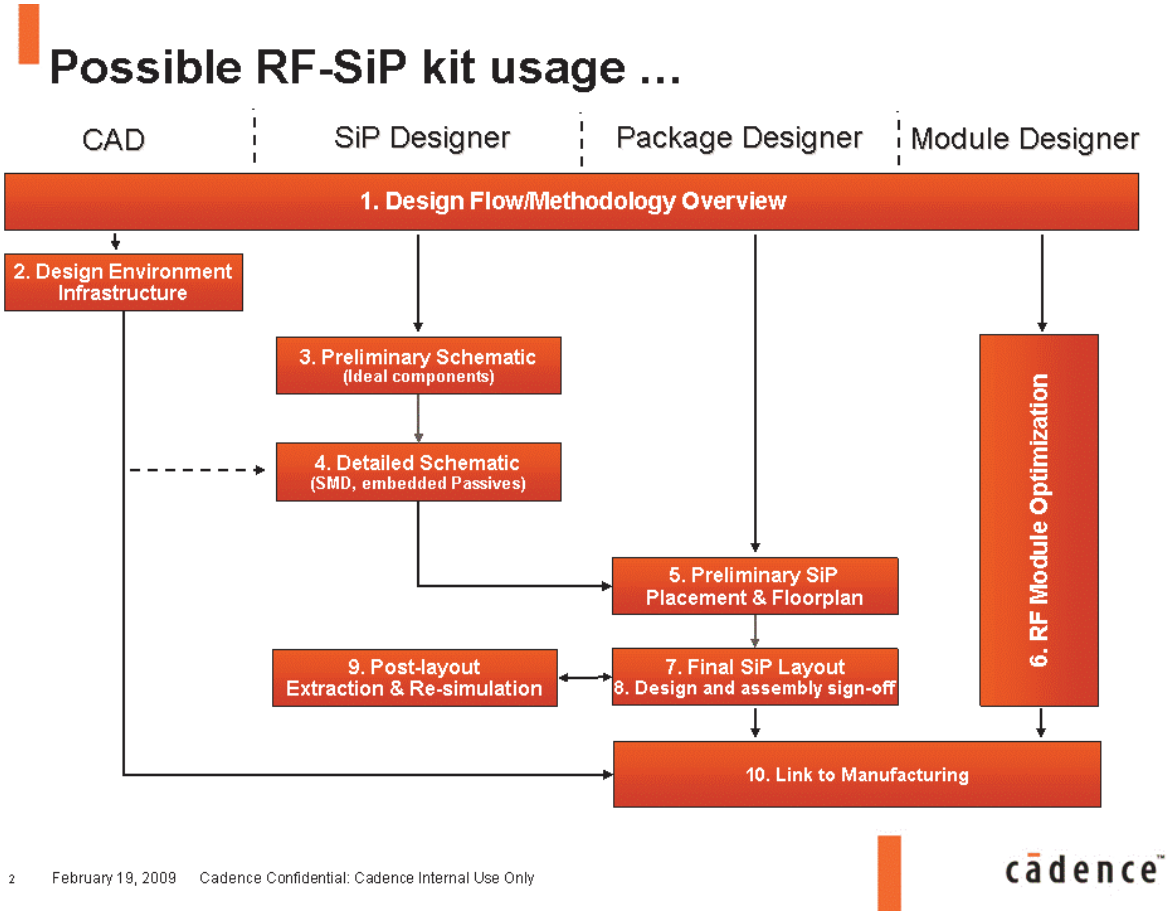
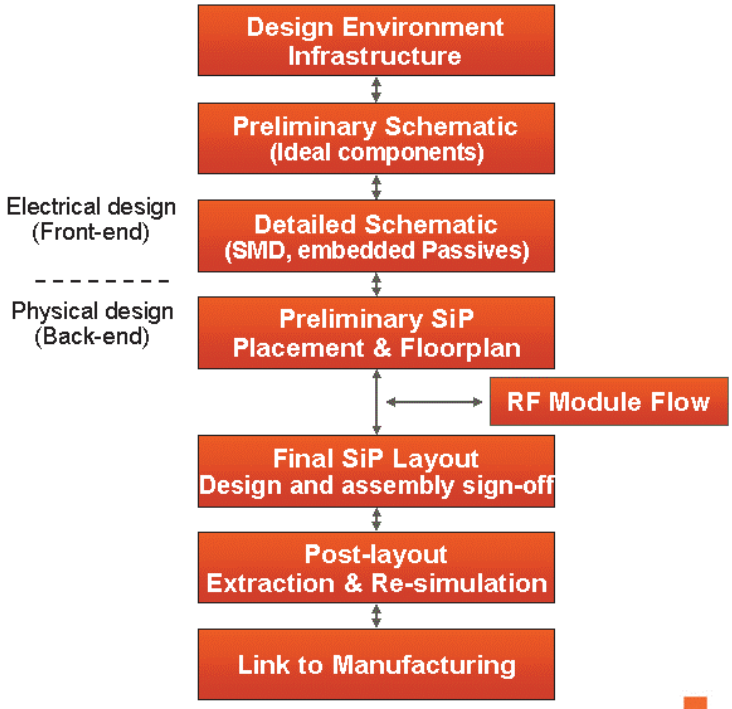
RF-SiP kit training agenda

The RF-SiP Kit has in total 10 modules (see pictures below) covering different design tasks in the RF-SiP flow. The modules are independent from each other. Means each module can be started without having to do or to complete a previous module.
The training will be done using the RF-SiP kit Userguide, a 600 pages document.
Each module describes in depth the design tasks and the students will exercise these design tasks step by step on the RF-SiP kit database.
Module 1 which gives an overview of the whole flow will be given as a live demo
It's planned to cover the theory of each module in presentations to all students.
After that the students will exercise the step by step instructions described in the RF-SiP Kit userguide at the workstations.
From a learning perspective the whole flow is covered in modules 2 -5 and 7-10. It would be challenging but feasible to cover all of these modules within 3 days.
Module 6 describes the whole RF-SiP flow, designing an LTCC power amplifier submodule or package. The flow steps aren't described in such a detail as in the other modules but this module would be a vehicle you could use for tutorials.



RF SiP Kit Tasks Flow



cadence

The agenda proposal for the 3 day RF-SiP kit training is shown below.
The first day covers mostly the SiP front-end design and simulation tasks done in the Virtuoso environment.
The second day covers the SiP back-end design tasks done in SiP layout environment.
The third day it will be shown how to design a power amplifier module (front-end & back-end) using the complete RF-SiP design flow.

RF-SiP kit training day 1

- RF-SiP kit Introduction & Demo
  Module 1: RF-SiP Design Flow & Methodology
- Module 2: Design Environment & Infrastructure
- Module 3: RF-SiP Front-End Design
- Module 4: Off-Chip Component Design

RF-SiP kit training day 2

- Module 5: Preliminary SiP Placement & Floorplan
- Module 7: SiP Layout Finishing
- Module 8: Post-Layout Extraction for Re-Simulation
- Module 9: Physical Design & Assembly Sign-Off
- Module 10: Links to Manufacturing

RF-SiP kit training day 3

- Module 6: RF Module Design
- Q&A